



PATENT
Attorney Docket No.: 020488-001000US
Client Ref. No.: 90U005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Q/A
TB sept 26
3-12-03

In re application of:

Chang Wan Ha

Application No.: 09/938,410

Filed: August 23, 2001

For: FLASH MEMORY HAVING A
FLEXIBLE BANK PARTITION

Examiner: Hoai V. Ho

Art Unit: 2818

AMENDMENT

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TECHNOLOGY CENTER 2800

Assistant Commissioner for Patents
Washington, D.C. 20231

February 26, 2003

Sir:

In response to the Office Action mailed September 26, 2002, please
amend the above-identified application as follows:

IN THE CLAIMS:

Please cancel claims 1-10 and add following new claims 11-27.

11. An integrated circuit memory comprising:

a plurality of memory arrays partitioned into first and second memory
banks in correspondence with one of a plurality of mask options such that the first
memory bank includes at least one but less than all of the plurality of memory arrays and
the second memory bank includes a corresponding remainder of the plurality of memory
arrays.

al
cont

12. The integrated circuit memory of claim 11 wherein each of the
plurality of mask options corresponds to a different partitioning of the memory arrays
into the first and second memory banks.